

Nickolai Andrianov

process engineer (technologist) в JSC "Svetlana-Rost"

W: <http://www.worky.com/nickolai-andrianov>

Nickolai Andrianov's Professional Experience

2009 - Present

JSC "Svetlana-Rost" - process engineer (technologist)

- Semiconductor technology: GaN, GaAs FET
- Plasma Processing: 1. Dry Etching 2. PECVD, ICPCVD 3. Ion Implantation
- Process Optimization
- Design of Experiment
- Data Analysis
- Gas Discharge Diagnostics: planar retarding field energy analyzer for plasma reactors and nuclear fusion reactors
- Gas Discharge computer simulation ACECFD
- Material Diagnostics: 1. SEM 2. AFM 3. Profilometry 4. SIMS 5. Elipsometry 6. IR spectroscopy
- Vacuum science and technology
- Equipment repair
- Clean room work

Key Skills

data, data analysis

Nickolai Andrianov's Education and Qualifications

2014

**PhD/Doctorate - Doctor of Philosophy (PhD) student, plasma physics
Saint-Petersburg State Politechnical University**

2009

**Bachelor/Degree - Master's degree, Physics, quantum electronics
Saint-Petersburg State University**

2006

**Bachelor/Degree - Bachelor's degree, Physics, quantum electronics
Saint-Petersburg State University**

Nickolai Andrianov's Additional Information

Links

Linkedin Profile - <http://www.linkedin.com/pub/nickolai-andrianov/50/529/ab5>
